

ALTERNATION HISTORY RECORDS

Date	Version	Mark	Page	Description	Drafter	Approver
2026-03-04	C	/	/	Release with a new version	Doris Chang	/

Part Numbering System :

<u>ATP</u>	<u>12</u>	<u>J</u>	<u>A</u>	<u>10K</u>
<u>Series Name</u> ATP:Thick-film Power Automotive Chip Resistors	<u>TypeInch (mm)</u> 02-0402(1005) 03-0603(1608) 05-0805(2012) 06-1206(3216) 10-1210(3225) 0A-2010(5025) 12-2512(6432)	<u>Tolerance</u> F= $\pm 1.0\%$ J= $\pm 5.0\%$ P=Jumper	<u>Package</u> Z=3Kpcs/10"Reel A=4Kpcs/7"Reel B=5Kpcs/7"Reel C=10Kpcs/7"Reel M=15Kpcs/7"Reel D=10Kpcs/10"Reel X=16Kpcs/13"Reel E=20Kpcs/13"Reel	<u>Resistance</u> 1R2=1.2 Ω 10K=10K Ω 10K5=10.5K Ω 100K=100K Ω 1M2=1.2M Ω

FEATURE

- 1) High power rating
- 2) Automotive grade AECQ-200 compliant
- 3) Reduced size of final equipment
- 4) RoHS 2 compliant with exemption 7C-I and Halogen free products
- 5) 100% CCD inspection

APPLICATION

- 1) High accuracy dc-power supply
- 2) Digital multi-meter
- 3) Telecommunication
- 4) Computer
- 5) Automotive industry
- 6) Medical and military equipment

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

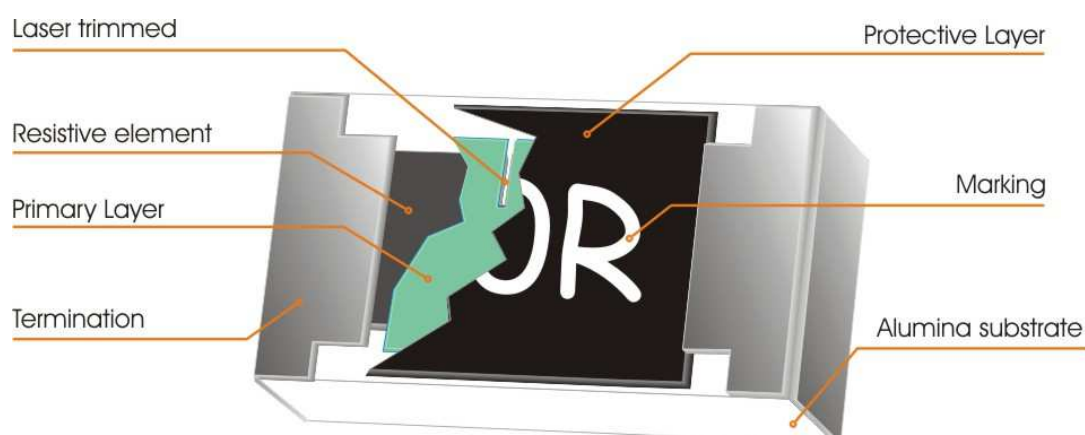


Fig 1. Construction of Chip-R

QUICK REFERENCE DATA

Item	General Specification						
Series No.	ATP02	ATP03	ATP05	ATP06	ATP10	ATP0A	ATP12
Size code	0402 (1005)	0603 (1608)	0805 (2012)	1206 (3216)	1210 (3225)	2010 (5025)	2512 (6432)
Resistance Tolerance	±1%, ±5%						
Resistance Range	0Ω, 1Ω~ 1MΩ (E96+E24 series)						
TCR (ppm/°C) 20°C ~ +155°C 10.2Ω ~ 1MΩ 1Ω ~ 10Ω	≤ ± 100 -200~+400	≤ ± 100 ≤ ± 100	≤ ± 100 ≤ ± 100	≤ ± 100	≤ ± 100 ≤ ± 200	≤ ± 100	
TCR (ppm/°C) 20°C ~ -55°C 10.2Ω ~ 1MΩ 1Ω ~ 10Ω	≤ ± 100 -200~+400	≤ ± 100 ≤ ± 150	≤ ± 100 ≤ ± 150		≤ ± 100 ≤ ± 200		
Max. dissipation at T _{amb} =70°C	1/10W	1/8 W	1/4 W	1/2W	1/2W	1W	2W
Max. Operation Voltage	50V	50V	150V	200V	200V	200V	300V
Max. Overload Voltage	100V	100V	300V	400V	400V	400V	500V
Operation temperature	-55 ~ +155°C						

Remark :

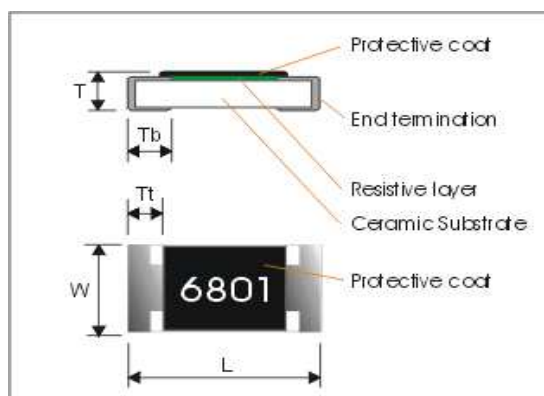
1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}}$$
or Max. RCWV listed above, whichever is lower.

TEST CONDITIONS FOR JUMPER (0 Ω)

Item	ATP12	ATP0A	ATP10	ATP06	ATP05	ATP03	ATP02
Power Rating At 70°C	2W	1W	1/2W	1/2W	1/4W	1/8W	1/10W
Resistance	Max. 20mΩ						
Rated Current	10A	7A	5A	5A	3.5A	2.5A	1A
Peak Current	25A	17.5A	12.5A	12.5A	8.8A	6.2A	2A
Operating Temperature	-55 ~ +155°C						

DIMENSIONS (unit : mm)



Part No	ATP02	ATP03	ATP05	ATP06	ATP10	ATP0A	ATP12
L	1.00 ± 0.05	1.60 ± 0.10	2.00 ± 0.10	3.10 ± 0.15	3.10 ± 0.10	5.00 ± 0.20	6.30 ± 0.20
W	0.50 ± 0.05	0.80 ± 0.10	1.25 ± 0.10	1.60 ± 0.15	2.60 ± 0.10	2.50 ± 0.20	3.10 ± 0.20
T	0.35 ± 0.05	0.45 ± 0.15	0.50 ± 0.15	0.55 ± 0.10	0.55 ± 0.10	0.60 ± 0.10	0.60 ± 0.15
Tt	0.20 ± 0.10	0.30 ± 0.10	0.40 ± 0.20	0.50 ± 0.25	0.50 ± 0.20	0.60 ± 0.25	0.60 ± 0.25
Tb	0.25 ± 0.10	0.30 ± 0.15	0.40 ± 0.20	0.50 ± 0.25	0.50 ± 0.20	0.60 ± 0.25	1.80 ± 0.25

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96&E24 series for resistors with a tolerance of $\pm 1\%$, $\pm 5\%$. The values of the E96/E24 series are in accordance with "IEC publication 60063".

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

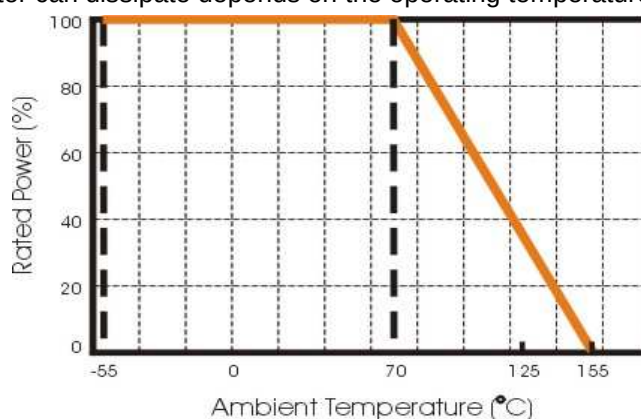


Fig.2 Maximum dissipation in percentage of rated power
As a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

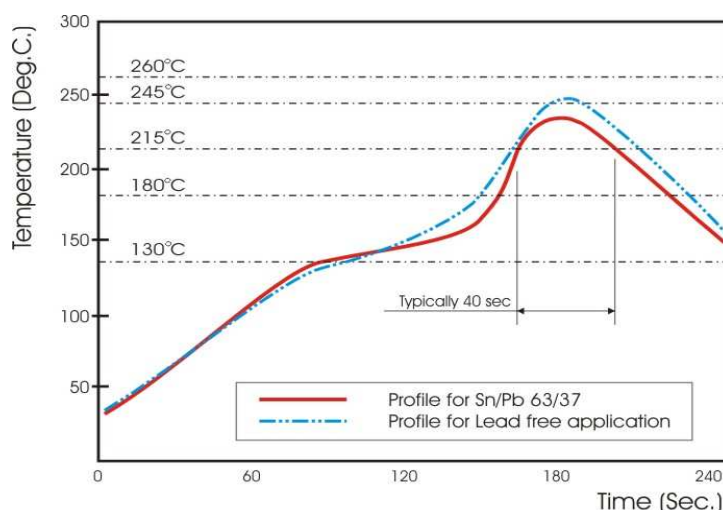
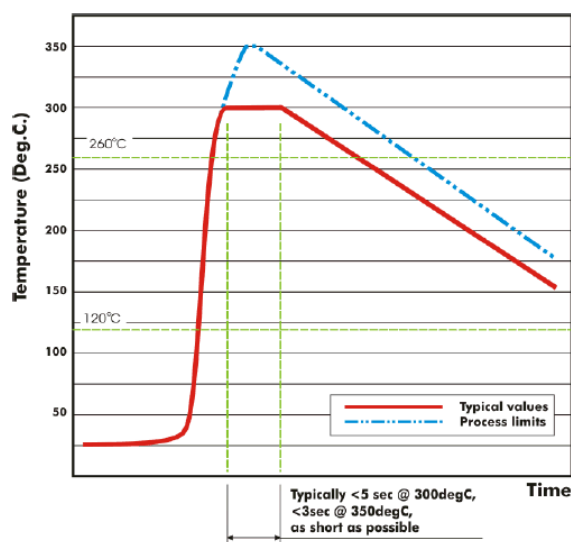
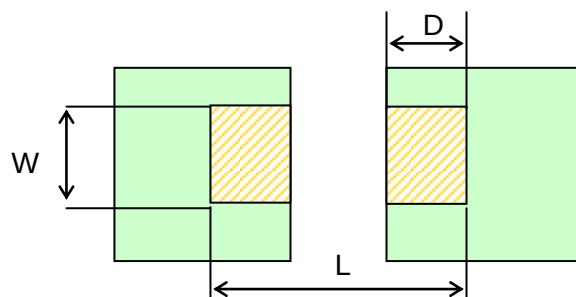


Fig 3. Infrared soldering profile for Chip Resistors

Manual soldering (solder iron)



Recommended Solder Pad Dimensions



Type	W (mm)	L (mm)	D (mm)
02(0402)	0.5~0.6	1.4~1.6	0.4~0.6
03(0603)	0.7~0.9	2.0~2.2	0.8~1.0
05(0805)	1.0~1.4	3.2~3.8	0.9~1.4
06(1206)	2.0~2.4	4.4~5.0	1.2~1.8
10(1210)	2.0~2.4	4.4~5.0	2.3~3.5
0A(2010)	3.3~3.7	5.7~6.5	2.3~3.5
12(2512)	3.6~4.0	7.8~8.6	2.3~3.5

Remark :

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

MARKING

Size \ Nr. Of digit of code\ tolerance	±5%	±1%
2512 (6432)	3-digits marking	4-digits marking
2010 (5025)	3-digits marking	4-digits marking
1210 (3225)	3-digits marking	4-digits marking
1206 (3216)	3-digits marking	4-digits marking
0805 (2012)	3-digits marking	4-digits marking
0603 (1608)	3-digits marking	3-digits marking
0402 (1005)	No marking	

3-digits marking (±5% : 2512, 2010, 1210, 1206, 0805, 0603)

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3-digits marking (±1% : 0603)

Nominal resistance				Description											
1.E-24 series				As 0603 CR03 ±5%.											
2.E-96 series				The 1st two digit codes are referring to the CODE on the table, the 3rd code is the index of resistance value : Y=10 ⁻² , X=10 ⁻¹ , A=10 ⁰ , B=10 ¹ , C=10 ² , D=10 ³ , E=10 ⁴ , F=10 ⁵ EX : 17.8Ω=25X, 178Ω=25A, 1K78=25B 17K8=25C, 178K=25D, 1M78=25E											
3. Remark				There is no marking for the items are not under E-24 and E-96 series											
CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking (±1% : 2512/ 2010/ 1210/ 1206/ 0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	10Ω	12Ω	100Ω	6800Ω	47000Ω
3-digits marking (1206 & 0805 & 0603 ±5%)	100	120	101	682	473
4-digits marking	10R0	12R0	1000	6801	4702

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : **Lower Category Temperature**, **Upper Category Temperature**; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

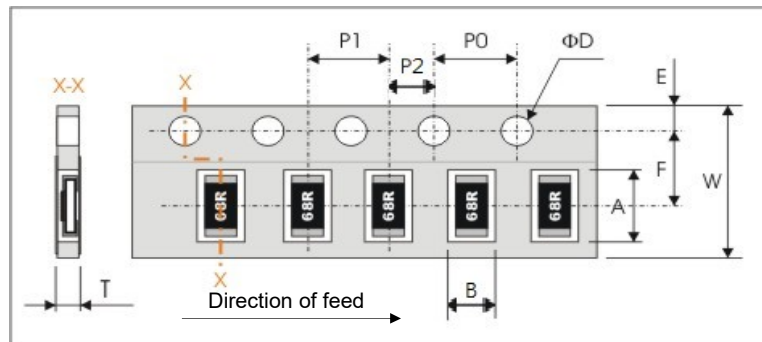
All soldering tests are performed with midly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT
		Resistor
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} \quad t_1 : 20^\circ\text{C} + 5^\circ\text{C} - 1^\circ\text{C}$ R_1 : Resistance at reference temperature R_2 : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"
Resistance to soldering heat(R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(1.0%+0.05Ω) no visible damage
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec. b) Steam the sample dwell time 8 hour/ solder dipping 215°C/ 5sec. c) Steam the sample dwell time 8 hour/ solder dipping 260°C/ 7sec.	95% coverage min., good tinning and no visible damage
Temperature cycling JESD22 Method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 30min maximum.	ΔR/R max. ±(1.0%+0.05Ω) No visible damage
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(1.0%+0.05Ω) No visible damage
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(2.0%+0.10Ω) No visible damage
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(2.0%+0.1Ω) No visible damage
High Temperature Exposure MIL-STD-202 Method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(2.0%+0.10Ω) No visible damage
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4),bending once 2mm for 60sec.	ΔR/R max. ±(1.0%+0.05Ω) No visible damage

Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C / dwell time 15min/ Max transfer time 20sec 300cycles	$\Delta R/R$ max. $\pm(1.0\%+0.05\Omega)$ No visible damage
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	$\Delta R/R$ max. $\pm(1.0\%+0.05\Omega)$ No visible damage
Mechanical Shock MIL-STD-202 method 213	Test ½ Sine Pulse, Peak value: 100g, normal duration: 6ms, Velocity change:12.3ft/sec. Three shocks in each direction, total 18 shocks.	Within product specification tolerance and no visible damage.
Vibration MIL-STD-202 method 204	Test 5g's for 20 min., 12 cycles each of 3 orientations.	$\Delta R/R$ max. $\pm(1\%+0.05\Omega)$ and no visible damage
Resistance to Solvents : MIL-STD-202 Method 215	Solvent is Isopropyl alcohol, immersion 3mins at 25°C and brush 10 strokes with a toothbrush with a handle made of a non-reactive material (wet bristle), immersion and brush 3 times and then air blow dry.	No superficial defect on marking, encapsulation, coating, appearance. Electrical characteristics within products specification and tolerance. Inspect at 3X max. for marking, inspect at 10X for part damage.
External Visual MIL-STD-883 method 2009	Electrical test not required. Inspect device construction, marking and workmanship	No visual damage and refer marking code.
Physical Dimension JESD22 method JB-100	Verify physical dimensions(L, W, T, Tb, Tt)	Within the specified tolerance

PACKAGING

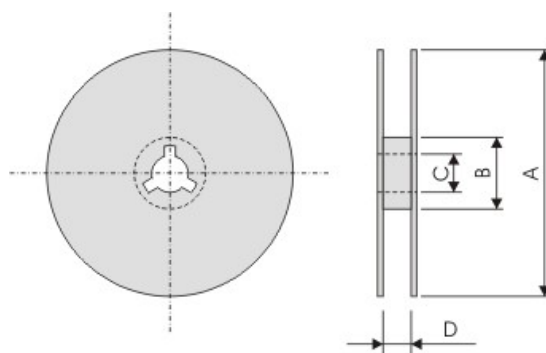
Tape specifications (unit :mm)



Series No.	A	B	W	F	E
ATP12	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
ATP0A	5.50±0.20	2.80±0.20			
ATP10	3.60±0.20	3.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
APT06	3.60±0.20	2.00±0.20			
ATP05	2.40±0.20	1.65±0.20			
ATP03	1.90±0.20	1.10±0.20			
ATP02	1.20±0.10	0.70±0.10			

Series No.	P1	P0	P2	ΦD	T
ATP12	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	Max.1.2
ATP0A					Max.1.2
ATP10/ATP06/ATP05	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	Max.1.0
ATP03					0.65 ±0.05
ATP02	2.00±0.05	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	0.40±0.05

Reel dimensions



Reel	A	B	C	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	14.0±0.20 (12mm tape) 9.0±0.5 (8mm tape)
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	
13" reel	Φ330.0±2.0	Φ100.0±1.0	13.0±0.2	

Tape packaging :

Size	Packaging tape	Packaging code			B: poly-bag
		7"reel	10"reel	13"reel	
2512,2010	12mm width plastic tape	4,000pcs	8,000pcs	16,000pcs	5000pcs
1210,1206,0805,0603	8mm width paper tape	5,000pcs	10,000pcs	20,000pcs	5000pcs
0402		10,000pcs	-	70,000pcs	5000pcs
0201		15,000pcs	-	70,000pcs	5000pcs
1218	12mm width plastic tape	-	3,000pcs	-	5000pcs

Performance of Taping :

Strength of Carrier Tape and Top Cover Tape

-Carrier Tape

When a tensile force 1.02kgf is applied in the direction of unreeling the tape, the tape shall withstand this force.

-Top cover Tape

When a tensile force 1.02kgf is applied to the tape, the tape shall withstand this force.

Peel Force of Top Cover Tape

Unless otherwise specified, the peel force of top cover tape shall be 10.2 to 71.4 g f when the top cover tape is pulled at a speed of 300mm/min with the angle between the taped during peel and the direction of unreeling maintained at 165 to 180° as illustrated in Fig.

