ALTERNATION HISTORY RECORDS 变更记录

Date 日期	Version 版本	Mark 标记	Page 页码	Description 描述	Drafter 制定者	Approver 审批者
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Part Numbering System):

<u>RCP</u>	<u>06</u>	$rac{\mathbf{F}}{ }$	${f B}_{overyon}$	<u>10K</u>
Series Name RCP: Thick Film High Power Lead Free Chip Resistors RoHS exemption free and Lead <100ppm	Type Inch (mm) 02-0402(1005) 03-0603(1608) 05-0805(2012) 06-1206(3216)	Tolerance $B=\pm 0.1\%$ $D=\pm 0.5\%$ $F=\pm 1\%$ $J=\pm 5\%$ P: Jumper	Package A=4Kpcs/7"Reel B=5Kpcs/7"Reel C=10Kpcs/7"Reel	Resistance $100=10\Omega$ $103=10$ ΚΩ

FEATURE

- 1. High power rating
- 2. High reliability and stability
- 3. RoHS exemption free and Lead free products

APPLICATION

- 1. High accuracy dc-power supply
- 2. Digital multi-meter
- 3. Telecommunication
- 4. Computer
- 5. Automotive industry
- 6. Medical and military equipment

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

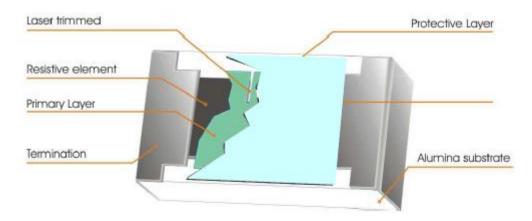


Fig 1. Construction of Chip-R



QUICK REFERENCE DATA

Item	General Specification			
Series No.	RCP02	RCP03	RCP05	RCP06
Size code	0402 (1005)	0603 (1608)	0805 (2012)	1206 (3216)
Resistance Tolerance		±1%,	±5%	
Resistance Range	0Ω,1Ω~ 1MΩ (E96+E24 series)	0Ω,1Ω~ 1MΩ (E96+E24 series)	0Ω,1Ω~ 10MΩ (E96+E24 series)	0Ω ,1 Ω ~ 10M Ω (E96+E24 series)
TCR (ppm/°C)				
$10Ω \sim 10MΩ$	≤ ± 100 ppm/°C	≤ ± 100 ppm/°C	≤ ± 100 ppm/°C	≤ ± 100 ppm/°C
< 10Ω	≤ ± 200 ppm/°C	≤ ± 150 ppm/°C	≤ ± 150 ppm/°C	≤ ± 100 ppm/°C
Max. dissipation at T _{amb} =70°C	1/8W	1/8W	1/4 W	1/2 W
Max. Operation Voltage	50V	75V	150V	200V
Max. Overload Voltage	100V	150V	300V	400V
Operation temperature	-55 ~ +155'C			

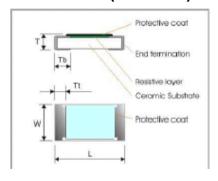
Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by $RCWV = \sqrt{RatedPower \times Resistance\ Value} \ or\ Max.\ RCWV\ listed\ above,\ whichever\ is\ lower.$

TEST CONDITION FOR JUMPER (0 Ω)

Item	RCP06	RCP05	RCP03	RCP02	
Power Rating	1/2W	1/4W	1/8W	1/8W	
Resistance	Max. 20mΩ	Max. 20mΩ	Max. 20mΩ	Max. 50mΩ	
Rated Current	5A	4A	2A	1.5A	
Peak Current	12.5A	10A	5A	3.8A	
Operating temperature	-55 ~ +155°C				

DIMENSIONS(unit:mm)



Part No	RCP02	RCP03	RCP05	RCP06
L	1.00 ± 0.05	1.60 ± 0.10	2.00 ± 0.10	3.10 ± 0.15
W	0.50 ± 0.05	0.80 ± 0.10	1.25 ± 0.10	1.60 ± 0.15
Т	0.35 ± 0.05	0.45 ± 0.15	0.50 ± 0.15	0.55 ± 0.10
Tb	0.25 ± 0.10	0.30 ± 0.15	0.40 ± 0.20	0.50 ± 0.25
Tt	0.20 ± 0.10	0.30 ± 0.10	0.40 ± 0.20	0.50 ± 0.25

MARKING

All series are defined as no marking!

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96&E24 series for resistors with a tolerance of $\pm 1\%, \pm 5\%$. The values of the E96/E24 series are in accordance with "IEC publication 60063".

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

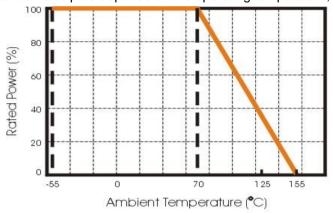


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

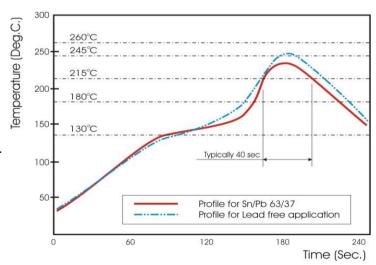


Fig 3. Infrared soldering profile for Chip Resistors



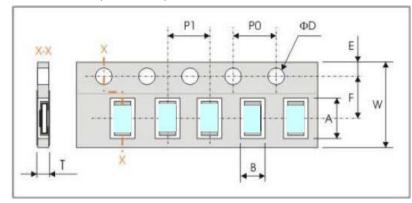
TEST AND REQUIREMENTS

Basic specification : JIS C 5201-1 : 1998

TEST	PROCEDURE	REQUIREMENT
Clause 4.8 Temperature Coefficient of Resistance (TCR)	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6 \ \ (\text{ppm/°C})$ $R_1: \text{Resistance at reference temperature}$ $R_2: \text{Resistance at test temperature}$ $t_1: 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$	Refer to quick reference data for T.C.R specification
Clause 4.13 Short time overload	5.0x Rated power or Max. Overload Voltage for 5 sec. Measure resistance after 30 minutes	Δ R/R max. J: \leq ±(2%+0.1 Ω) F: \leq ±(1%+0.05 Ω)
Clause 4.18 Resistance to soldering heat	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C $\pm 5^{\circ}\text{C}$	No visible damage $ \Delta R/R \text{ max. J:} \leqq \pm (1\% + 0.1\Omega) $ $ F: \leqq \pm (0.5\% + 0.05\Omega) $
Clause 4.17 Solderability	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235°C±5°C	Good tinning (>95% covered) No visible damage
Clause 4.18 Leach Test	Un-mounted chips completely immersed for 60±1second in a solder bath at 260 $^{\circ}\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!$	Ditto
Clause 4.19 Temperature cycling	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	No visible damage $ \Delta R/R \text{ max. J} \leq \pm (1\% + 0.1\Omega) $ $ F \leq \pm (0.5\% + 0.05\Omega) $
Clause 4.25 Load life (endurance)	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller $70\pm2^{\circ}\text{C}$, 1.5 hours on and 0.5 hours off	No visible damage $ \Delta R/R \text{ max. } J \leqq \pm (3\% + 0.1\Omega) $ $ F \leqq \pm (1\% + 0.05\Omega) $
Clause 4.24 Load life in Humidity	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	No visible damage $ \Delta R/R \text{ max. } J \leqq \pm (3\% + 0.1\Omega) $ $ F \leqq \pm (1\% + 0.05\Omega) $
Clause 4.33 Bending strength	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 2 mm(2512;2010) 3mm(1206), once for 10 seconds	No visible damage $\Delta R/R$ max. $J \le \pm (1\% + 0.1\Omega)$ $F \le \pm (0.5\% + 0.05\Omega)$
Clause 4.32 Adhesion	Pressurizing force: 5N, Test time: 10±1sec	No remarkable damage or removal of the terminations
Clause 4.6 Insulation resistance	Apply the maximum overload voltage (DC) for 1minute	R≥10GΩ
Clause 4.7 Dielectric withstand voltage	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover

PACKAGING

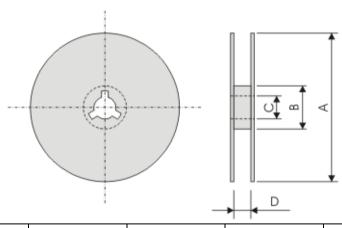
Paper Tape specifications (unit :mm)



Series No.	Α	В	W	F	E
RCP06	3.60±0.20	2.00±0.20 8.00±0.30		3.50+0.20 1.75+	1.75±0.10
RCP05	2.40±0.20	1.65±0.20	6.00±0.30	3.50±0.20	1.75±0.10
RCP03	1.90±0.20	1.10±0.20	8.00±0.30	3.50±0.20	1.75±0.10
RCP02	1.20±0.10	0.70±0.10	8.00±0.30	3.50±0.20	1.75±0.10

Series No.	P1	P0	ΦD	Т
RCP06	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	Max. 1.0
RCP05	4.00±0.10	4.00±0.10	$\Psi 1.50_{-0.0}$	Max. 1.0
RCP03	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	0.65±0.05
RCP02	2.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	0.40±0.05

Reel dimensions



Symbol	А	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5